

International Journal of Advanced Research in Computer Science

RESEARCH PAPER

Available Online at www.ijarcs.info

Performance Evaluation and Rate of Acceptance of Various Multistage Interconnection Networks

Lovnish Bansal^{*} and Shruti Goel CSE Department, Bhai Maha Singh College of Engg. Muktsar (Punjab) India lovnish bansal@rediffmail.com, er.shrutigoel1@gmail.com

Abstract: This paper states the various methods to evaluate performance of various Multistage Interconnection Networks. A number of Interconnection Networks are studied to get the most reliable network. We have implemented three different algorithms to find the reliability of a network and to get the probability of acceptance of a Multistage Interconnection Network. We also come to the conclusion that the Irregular Multistage Interconnection Networks are more reliable than Regular Multistage Interconnection Network because the number of stages in Irregular Multistage Interconnection Networks are lesser than that of Regular Multistage Interconnection Networks .

A Number of Performance factors are used like Bandwidth, Probability of Acceptance and cost of regular and irregular interconnection networks. We will also compute reliability of various Multistage Interconnection Networks with and without repairing of switching elements.

Keywords: Multistage Interconnection Network, Bandwidth, Performance, Reliability, Switching elements.

I. INTRODUCTION

Multistage interconnection network is a set of connections between multiprocessor systems, ATM switches, or Gigabit Ethernet switches. And each Switch is a cross bar Network. Various types of Regular and Irregular Multistage Interconnection networks are:-

- a) Omega Network
- b) Augmented Shuffle Exchange Network
- c) Augmented Baseline Network
- d) Zeta Network

A Multistage Interconnection Network consist of number of stages as shown in Fig.1.



Fig.1 Multistage Interconnection Network

The main objective of Interconnection Network is to design a network that combines full connection capability with graceful degradation in spite of the existence of faults. A switch in Multistage Interconnection Network can work in either T-mode to provide parallel connection or in X-mode to provide cross connection as shown in Fig.2.



Figure 2 T-mode and X-mode of Switch

Various Connection Issues in Multistage Interconnection Networks are Bandwidth, Probability of acceptance, Throughput, Reliability, Permutation, Cost, Path length, Fault tolerance and performance. Reliability of an Interconnection Network can be measured in two ways:-

- a) Series Reliability Model
- b) Parallel Reliability Model

A. Series Reliability model

A system is said to be in series if all the component of this are involved during the operation. The failure of any component means failure of the whole system. Reliability of a series type of system is shown in Fig.3.



Figure 3 Series system

It is given by the product of reliabilities of individual component in it.

 $R_{\text{SERIES}}(t) = P_1(t) \cdot P_2(t) \cdot P_3(t) \dots P_n(t) = \prod P_i(t)$ Where P_i is the reliability of IN component

B. Parallel Reliability Model

A system is said to be in parallel if the successful functioning of any of the component leads to the success of the system. System fails only when the entire component fails.



Figure 4 Parallel System

Reliability of a parallel type of system is given by

 $R_{\text{parallel}}(t) = 1 - \Pi(1-P_i(t))$ Where P_i is the reliability of IN component.

We can classify Multistage Interconnection Networks (MINs) as:-

- a) Unique Path MINs
- b) Multi-path MINs
- c) Regular MINs
- d) Irregular MINs
- e) Static MINs
- f) Dynamic MINs

a) Unique Path MIN: - Unique paths MINs are characterized by the presence of single unique path between any source destination pair.

b) Multi-Path MIN: - The multiple paths between each inputoutput pair of the network are provided by connecting switching elements within the same stage. This technique is called chaining. The chained network possesses alternate paths that are available at each switching element, thus the network is strongly re-routable.

c) Regular Network: - Any network is said to be regular if the number of switching elements (SEs) in different stages of the network is same.

d) Irregular Network:- Any network is said to be irregular if the number of switching elements (SEs) in different stages of the network is different.

e) Static Interconnection Network: - In a static interconnection network, links among different nodes of system are passive.

f) Dynamic Interconnection Networks: Dynamic networks are built using switches and communication links.

II. METHODOLOGY

A. Omega Network

The Omega network is one particular kind of multistage network; it is a unique path interconnection network which provides a distinctive path between any input and output. Omega Network has $N = 2^n$ inputs, termed sources (S), and 2^n outputs termed destinations (D). There is a unique path between each source-destination pair. The Omega Network has n stages and each stage has N/2 switching elements. The net-

work complexity, defined as the total number of switching elements in the MIN, is $(N/2)(\log_2 N)$.

Each stage implements a perfect shuffle wire interconnection followed by N/2 switching elements, where each switch is capable of sending either input to either output. It is assumed that each input to a switch can receive at most one input, and that the switches do not contain queues. For presentational convenience, it is assumed that a switch cannot send a given input to both outputs, but the results do not depend on this assumption. In Fig. 5, various interconnection links for each stage from the top down with an N-bit binary address, and have been labeled shown with two paths through the network.



Figure 58X8 Omega Network

B. Routing in Omega Network

Let $S_{m-l}, S_{m-2}, ..., S_O$ and $d_{m-b}, d_{m-2}, ..., dO$ be the binary representations of the source and destination addresses respectively, where *m* is the number of address bits ($m = log_2n$). The switches along the path in stage i use the bit d_{m-i} and make a connection to the upper output link if the bit is 0 or to the lower output link if it is 1. Fig.6 shows how a cell is routed form *S2* to *D4*. Routing is represented as the bitwise shuffle-exchange shown in Fig. 7. The resulting address after a shuffle corresponds to the switch input at each stage, and the resulting address after an exchange corresponds to the switch output at each stage. A complete routing requires *m* shuffle-exchanges until the packet reaches its destination. The address labels in Fig.7 shows an example of this operation.



Figure 7 Shuffle Connection between stages

C. Performance Analysis

Assume a MIN of size $a_n \times b_n$ constructed from a $\times b$ crossbar switches and having an Sources connected to destinations. The analysis of crossbar is applied to a $\times b$ crossbar switch and then extended to the complete MIN. The distinct destination digit (in base b) for setting of individual a $\times b$ switches controls each stage of MIN.

The probabilistic approach is used to analyze the MINs based on independent request assumptions. Given the request rate p at each of the a inputs of an a x b crossbar module, the expected the rate of requests on any one of the b output lines from any one input is given by p/b.

Probability of not getting the request is:

$$1 - p/b$$

Probability of not getting the request from all 'a' inputs is given by:

$$(1 - p / b)^{a}$$

Probability of one output getting the request from all 'a' inputs is given by:

1 - (1 - p / b)

The total number of requests that it passes per unit time is given by

 $b - b(1 - p / b)^{a}$

Since the output rate of a stage is the input rate of the next stage, output rate of any stage can be recursively calculated starting from stage 1 and the output rate of final stage n determines the bandwidth of MIN

D. Probability Equations for Omega Network

Probability equations for 16×16 Omega Network is given by:

$$p0 = p$$

$$p1 = 1 - (1 -p0 /2)^{2}$$

$$p2 = 1 - (1 -p1 /2)^{2}$$

$$p3 = 1 - (1 -p2 /2)^{2}$$

$$p4 = 1 - (1 -p3 /2)^{2}$$

$$p_{n} = p4$$

E. Reliability Analysis

Reliability of the network is concerned with the ability of a network to carry out its desired network operation successfully. Reliability of a system is the probability that it will perform its intended function satisfactorily for a given time under stated operating conditions. These networks are able to send messages through alternative paths when some faults are detected in the regular route. This reliability measure is sometimes referred to as the Source-to-Multiple Terminal (SMT) Reliability.

The reliability measures of particular interest are:

- Terminal Reliability (TR)
- Broadcast Reliability (BR)
- Network Reliability (NR).

a) Terminal Reliability (TR): Terminal reliability, generally used as a measure of robustness of a MIN, is the probability of existence of at least one fault free path between a designated pair of input (s) and output (t) terminals (two-terminal).

b) Broadcast Reliability (BR) Another useful measure of the reliability of a MIN is its ability to broadcast data from a given input terminal to all the output terminals of the network. A network is said to have failed when a connection cannot be made from the given input terminal to at least one of the output terminals.

c) Network Reliability (NR): The network reliability is defined as the probability that there exists a connection between each input to all outputs (all-terminal).

We have made some assumptions to calculate reliability using Markov Model:-

1) All SEs are substantially less reliable than the links. The SEs is dynamic solid-state devices that have a calculable imperfect reliability. Links are typically static devices such as backplanes or wires.

2) Each SE in the MIN has two states: good or bad.

3) The reliability of the SE is known. With a given $\lambda(t)$, using the exponential model one can compute the reliability p_i of the SE as:

$$p_i(t) = \exp\left(-\left[\int_0^t \lambda_i(u)du\right]\right),$$

4) When X (t) is constant, then:

$$p_i = \exp(-\lambda_i t).$$

5) The SEs can be repaired.

6) All failures are statistically-independent i.e. All the switch element are not depend on each other. 7) The failure of switch occur independently in a network with a failure rate of λ (= 10⁻⁶ per hour) pre unit time. Based on the gate count failure rate for 2×2 SE, $\lambda_2=\lambda$ and for 3×3 SE $\lambda_3 = 2.25\lambda$, for m: 1 multiplexer $\lambda_m = \lambda_d = \lambda/4 \times m =$ failure rate of 1: m demultiplexer. Now we can obtain the time-dependent reliability of PC by symbolically this 3-state Markov model:

(1)
$$R_{PC}(t) = Be^{-\alpha t} + Ce^{-pt}$$

(2) $\alpha = \frac{1}{2} \left[\mu + 3\lambda - \sqrt{\mu^2 + \lambda\mu (8c - 2) + \lambda^2} \right]$
(3)
 $\beta = \frac{1}{2} \left[\mu + 3\lambda + \sqrt{\mu^2 + \lambda\mu (8c - 2) + \lambda^2} \right]$
(4)
 $B = \frac{1}{\beta - \alpha} (\beta + 2\lambda (C - 1))$
(5)
 $C = -\frac{1}{\beta - \alpha} (\alpha + 2\lambda (C - 1))$

MTTF (Mean Time To Failure)

The mean time to failure is the integral under the reliability curve.

$$MTTF = \int_{0}^{\infty} R(t)dt$$

F. Reliability of Omega Network

The bounds on reliability are computed by applying Series-Parallel model of the concerned network, considering that any of the switching elements, multiplexers or de-multiplexers may fail.

The analysis is carried as under:

Let R_s (t), the reliability function of a component, be defined as the probability that a failure does not occur in the time period (0,t).

Reliability of Omega Network is:-



Figure 8 Reliability of Omega Network

MTTF is dependent on the network size N and on the real time operation 't'. By using 2x2 switching element MTTF for Omega network is evaluated from the following expression.

MTTF _{omega} =
$$\int_{0}^{x} [e^{-\lambda t}]^{N/2 * \log 2N} * dt$$

Table I. Reliability of omega network for different size of networks

| NETWORK SIZE | Reliability (MTTF $* \lambda$) | |
|--------------|---------------------------------|--|
| 4 | 0.25000 | |
| 8 | 0.08333 | |
| 16 | 0.03125 | |
| 32 | 0.01250 | |
| 64 | 0.00520 | |
| 128 | 0.00260 | |
| 256 | 0.00097 | |
| 512 | 0.00004 | |
| 1024 | 0.00019 | |

G. Shuffle Exchange Network (SEN)

SEN is a unique-path MIN, the failure of any switch causes system failure, so from the network reliability point of view, we have $(N/2)(\log_2 N)$ SEs in "series". Thus, imperfect coverage and repair are not factors in the computation of system reliability. Assuming a constant failure rate for each component, let λ_2 denote the failure rate of SE₂.

Hence, the time-dependent reliability and the mean time-to-failure of an $N \times N$ SEN is given by:

$$\boldsymbol{R}_{SEN}(t) = \left[e^{-\lambda_2 t}\right]^{\frac{1}{2}N \log_2 N}$$

$$MTTN_{SEN} = \frac{1}{\frac{1}{2}N\log_2 N}$$

Extra Stage Shuffle Exchange Network (SEN+)

Exact reliability analysis of the SEN+, even with perfect coverage and no repair, becomes intractable for networks larger than 16×16 . However, useful lower and upper bounds have been derived. We extend these reliability bounds to incorporate imperfect coverage and repair. By applying the composition of the subsystems as illustrated with the 2-component parallel system, we can determine the reliability and availability expressions.

H. Pessimistic (lower bound) analysis of SEN+

The switching elements in the intermediate stages of a SEN+ can be failed, and yet the network is still operational. We model the intermediate stages as a system consisting of a parallel arrangement of two series subsystems each with (N/4) $(\log_2 N - 1)$ switches. We have a series system of three subsystems-the first and last are series subsystems and the middle subsystem is a parallel-series subsystem.

The reliability expression using lower bound is given by

$$R_{SEN+_{LB}}(t) = e^{-N\lambda_{2}t} [B' e^{-\alpha_{2}t} + C' e^{-\beta_{2}t}]^{K}$$

The Mean Time to Failure is:

$$MTTF_{SEN+_{LB}} = \frac{B'}{\alpha' + N\lambda_2} + \frac{C'}{\beta' + N\lambda_2}$$

Where B', C', α ', β ' are determined by substituting ¹/₄ N(log₂N-1) λ_2 for λ inn equations (2) to (5).

I. ASEN-2Network

Augmented Shuffle Exchange Network (ASEN-2) is a regular network, having equal number of switches in each of the stage. ASEN-2 network is constructed from Shuffle Exchange Network by adding a stage of 2×1 multiplexers at the initial stage and 1×2 demultiplexers at last stage. It provides multiple paths between a source and a destination. ASEN-2 of size N × N with N number of sources and N number of destination consists of log2N -1 stages where the initial stage consists of N/2 switches of size 3×3 and the last stage consist of N/2 switches of size 2×2 .

ASEN-2 provides fault tolerance using links between the conjugate pairs of switches. A 16×16 ASEN- 2 network is shown in Fig.9.



Figure 9 Augmented Shuffle Exchange Network(ASEN-2)

The reliability expression using lower bound is given by:

$$R_{\text{ASEN}_{b}}(t) = \sum_{i=0}^{I} \sum_{j=0}^{J} \sum_{k=0}^{K} \left(\frac{I!}{i!(I-i)!} \right) \left(\frac{J!}{j!(J-j)!} \right) \left(\frac{K!}{k!(K-k)!} \right) \times \left[(B_{3m})^{i} (C_{3m})^{i-1} (B_{3})^{j} (c_{3})^{J-j} (B_{2m})^{k} (C_{2m})^{K-k} \right] \\ \times \exp\left[-\left\{ i \alpha_{3m}^{m} + (I-i) \beta_{3m}^{m} + j \alpha_{3}^{m} + (J-j) \beta_{3}^{m} + k \alpha_{2m}^{m} + (K-k) \beta_{2m}^{m} \right\} t \right]$$

Where $B_m, C_m, \alpha_m, \beta_m$ are determined by substituting λ_m for λ ; B_3, C_3 , α_3, β_3 are determined by substituting λ_3 for λ ; and $B_{2m}, C_{2m}, \alpha_{2m}, \beta_{2m}$ are determined by substituting λ_{2m} for λ in above equation, respectively I= N/2, J=N/4×(n-2) and K=N/4 where n=log₂N.

The mean time to failure is given by:

$$MTTF_{ASEN_{ub}}(t) = \sum_{i=0}^{I} \sum_{j=0}^{J} \sum_{k=0}^{K} \left(\frac{I!}{i!(I-i)!} \right) \left(\frac{J!}{j!(J-j)!} \right) \left(\frac{K!}{k!(K-k)!} \right) \times \frac{\left[(B_{m})^{i} (C_{m})^{i-1} (B_{3})^{j} (c_{3})^{J-j} (B_{2m})^{k} (C_{2m})^{K-k} \right]}{i\alpha_{m} + (I-i)\beta_{m} + j\alpha_{3} + (J-j)\beta_{3} + k\alpha_{2m} + (K-k)\beta_{2m}}$$

J. Augmented Baseline Network (ABN)

Exact reliability analysis of the ABN, even with perfect coverage and no repair, is tractable only for rather small (8×8) networks. For larger networks, useful lower and upper bound RBD models were initially proposed; these RBD models were extended to incorporate the added complexity of the SEs used in the ABN, the multiplexers and demultiplexers used at the input and output interfaces of the network, and preliminary reliability analysis which separately considered on-line repair and imperfect converge was presented.

We considered the 2×2 switch and its associated demultiplexers as a single component (SE_{2d}), so $\lambda_{2d} = 2\lambda$ can be assigned to this group of elements. Also let λ_3 be the failure rate for the 3×3 switch (SE₃), then based on gate count $\lambda_3 = 2.25\lambda$ and aggregated failure rate will be $\lambda_{3m} = 4.25\lambda$.

K. Optimistic (Upper bound) analysis of ABN

To obtain an upper bound for the ABN, we observe that each source is connected to two multiplexers and each SE has a conjugate. So if we assume that the ABN is operational as long as one of the two multiplexers attached to a source is operational and as long as both components in a conjugate pair are not faulty, then we will permit as many as one-half of the system components to fail and the ABN *can* still be operational reliability block. The upper bound of the ABN reliability is:

$$R_{BN_{b}}(t) = \sum_{i=0}^{I} \sum_{j=0,k=0}^{K} \left(\frac{I!}{i!(I-i)!} \right) \left(\frac{J!}{j!(J-j)!} \right) \left(\frac{K!}{k!(K-k)!} \right) \left\{ (B_{m}^{j}(C_{m}^{j+1}(B_{j}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j}(C_{3}^{j+1}(B_{3}^{j+1}(C_{3}^{j+1}(B_{3}^{j+1}(C_{3}^{j+1}(B_{3}^{j+1}(D_{3}^{j+1}(D_{3}^{j+1}(D_{3}^{j+1}(B_{3}^{j+1}(C_{3}^{j+1}(B_{3}^{j+1}(D_{3}^{j+1}(B_{3}^{j+1}(D_{3}^{j+1}($$

Where B_m , C_m , α_m , β_m are determined by substituting λ_m for λ . B_3, C_3 , α_3, β_3 are determined by substituting λ_3 for λ ; and B_{2d}, C_{2d} , α_{2d}, β_{2d} are determined by substituting λ_{2d} for λ in above equation, respectively I= N/2, J=N/4×(n-3) and K=N/4. Probability Equations for Augmented Baseline Network

Probability equations for 16×16 Augmented Baseline Network are given by:

$$p0 = p$$

p1 = 1- (1 -p0 /3)³
p2 = 1- (1 -p1 /2)²

L.Pessimistic (lower bound) analysis of ABN

At the input side of the ABN, the multiplexers are not considered an integral part of a given 3×3 SE. That is, a multiplexer *can* be failed, and as long as at least one of its two associated SEs is operational, the network *can* be operational. But if we group two multiplexers with each SE on the input side and consider them as a series system, then we have a conservative estimate of the reliability of these three components. Finally, these aggregated components and the SEs in the intermediate stages *can* be arranged in pairs of conjugate loops. To obtain the pessimistic (lower) bound on the reliability of the ABN, we assume the network is failed whenever more than one loop has a faulty element or more than one SE in a conjugate pair in the last stage fails. The ABN *can* tolerate any single loop failure or the failure of any single switch in the last stage.

For N \geq 8, the lower bound of the ABN is:

$$R_{ABV_{b}}(t) = \sum_{i=0}^{I} \sum_{j=0,k=0}^{J} \left(\frac{I!}{i(I-i)!} \right) \left(\frac{J!}{j(J-j)!} \right) \left(\frac{K!}{k(K-k)!} \right) \left((B_{3n})^{j} (C_{3n})^{j+1} (B_{3n})^{j} (C_{3n})^{k-j} (B_{3n})^{k} (C_{2n})^{k-j} \right) \right)$$

×exp[-{ $i\alpha_{3m} + (I-i)\beta_{3m} + j\alpha_{3} + (J-j)\beta_{3} + k\alpha_{3d} + (K-k)\beta_{2d} t$]

The mean time to failure is given by:

$$MTTF_{ABN_{lb}}(t) = \sum_{i=0}^{I} \sum_{j=0}^{J} \sum_{k=0}^{K} \left(\frac{I!}{i!(I-i)!} \right) \left(\frac{J!}{j!(J-j)!} \right) \left(\frac{K!}{k!(K-k)!} \right) \times \frac{\left[(B_{3m})^{i} (C_{3m})^{i-1} (B_{3})^{j} (c_{3})^{J-j} (B_{2d})^{k} (C_{2d})^{K-k} \right]}{i\alpha_{2} + (I-i)\beta_{2} + i\alpha_{2} + (I-i)\beta_{2} + k\alpha_{2d} + (K-k)\beta_{2d}}$$

 $i\alpha_{3m} + (I-i)\beta_{3m} + j\alpha_3 + (J-j)\beta_3 + k\alpha_{2d} + (K-k)\beta_{2d}$ Where B_{3m} , C_{3m} , α_m , β_m are determined by substituting λ_{3m} for λ . $B_3, C_3, \alpha_3, \beta_3$ are determined by substituting λ_3 for λ and $B_{2d}, C_{2d}, \alpha_{2d}, \beta_{2d}$ are determined by substituting λ_{2d} for λ in above equation, respectively I= N/8, J=N/8×(n-3) and K=N/4.

M. Calculation of Bandwidth and Probability of Acceptance

Bandwidth in computer networking refers to the data rate supported by the network connection or interface. One most commonly espresses bandwidth in terms of bits per second (bps.), the terms comes from the electrical engineering, where bandwidth represents the total distance or range between the highest or lowest signals on the communication channel (Band).

Bandwidth represents the capacity of the connection. The greater the capacity, the more likely that greater performance will be.

Above given probabilistic equations are utilized for the purpose of achieving some analytical results. So Bandwidth BW is given by: -

 $BW = p[n] \times Size of the Network$

Probability of acceptance (Pa) is defined as the ratio of expected bandwidth to the expected number of request generated by a source per cycle.

III RESULTS AND DISCUSSIONS

A. Cost Effectiveness

To estimate the cost of a network, one common method is to calculate the switch complexity with the assumption that the cost of a switch is proportional to the number of gates involved, which is roughly proportional to the number of cross points within a switch. For example 2×2 switch has 4 units of hardware cost, where as 3×3 has 9 units for the multiplexer and de-multiplexers. We roughly assume that each of $m\times 1$ multiplexer or $1\times m$ de-multiplexer has m units of cost.



Figure 10 Reliability Comparison between SEN+, ASEN and ABN Networks

Comparison of Reliabilities between SEN+, ASEN and ABN with perfect convergence and no repair.

Fig.10 shows the comparison of reliabilities between SEN+, ASEN and ABN with convergence and no repair.

Table 3. below shows the comparison of bandwidth of various MINs.

Table II. Networks with their cost functions

| NETWORK | COST |
|---------|----------------------|
| ESC | $2N(\log_2 N + C)$ |
| ASEN | $3N(1.5 \log_2 N-1)$ |

| ABN | $(9 \log_2 N-11)N/2$ |
|---------|--------------------------|
| ZTN | $N/8(56+24 \log_2(N/2))$ |
| MFT-New | $2N(\log_2 N+5)+N/2$ |

Table III. Comparison of Bandwidth between Omega, ASEN, ABN and MFT-New network

| Р | OMEGA | ASEN | ABN | MFT-New |
|-----|-------|------|------|---------|
| 0.1 | 1.45 | 1.46 | 1.51 | 1.42 |
| 0.2 | 2.64 | 2.69 | 2.85 | 2.55 |
| 0.3 | 3.63 | 3.71 | 4.04 | 3.46 |
| 0.4 | 4.45 | 4.58 | 5.09 | 4.20 |
| 0.5 | 5.13 | 5.31 | 6.03 | 4.81 |
| 0.6 | 5.71 | 5.92 | 6.85 | 5.31 |
| 0.7 | 6.17 | 6.45 | 7.58 | 5.73 |
| 0.8 | 6.57 | 6.9 | 8.22 | 6.08 |
| 0.9 | 6.92 | 7.28 | 8.78 | 6.38 |

IV CONCLUSION AND FUTURE SCOPE

- Irregular MINs are better than that of Regular MINs in terms of reliability.
- It is concluded that the performance of MINs with repair of switching elements is better than without repair (in terms of Reliability) on various MINs.
- Reliability of Multipath MIN's is more then Unique path MIN's. i.e Reliability of SEN+ is More then SEN with and without repair.
- As the Network size increases, Reliability in terms of MTTF of the MINs Decreases.
- Probability of Acceptance decreases as request generation probability increases.
- Irregular MINs are more Cost Effective as compared to regular MIN's.

Future Scope

- Reliability analysis can also be done of Optical MINs.
- By using Markov Model Availability analysis can be done for various types of regular and irregular MINs.
- The detailed Performance analysis can be done for various MINs by using other parameters Throughput, Permutation Possibility, Latency and Path length etc.
- A comparative analysis of irregular MINs with the regular MINs can be done by using another performance parameter.

V. REFERENCES

- [1] Sheta, Parameter Estimation of Software Reliability Growth Models by Particle Swarm Optimization, ICGST-AIML Journal, Vol. 7, No.7, June,2007, pp.55-61.
- [2] H. Aggarwal, and P.K. Bansal, Routing and Path Length Algorithm for cost effective Modified Four Tree Network, IEEE TENCON, 2002, pp. 293-294.
- [3] Harsh Sadawarti and P.K Bansal., "Fault tolerant Irregular Augmented Shuffle Network" WSEAS International Conference on Computer Engineering and

Applications, Gold Coast, Australia, Jan, 2007, pp. 17-19.

- [4] J. Sengupta and P.K Bansal, "Reliability and performance measures of regular and irregular Multistage Interconnection Networks", Interational Conference IEEE TENCON 2000, pp. 531-536.
- [5] J. Sengupta and P.K. Bansal, "Performance Analysis of Regular and Irregular Dynamic MINs", International Conference IEEE TENCON 99, Sept. 1999, Cheju Island, Korea, pp. 427-430.
- [6] Jose Duato, Sudhakar Yalamanchili and NiLionel, "Interconnection Networks: An Engineering Approach", IEEE Computer Society, 1997.
- [7] M. Lubazewski and B Coutois, "A Reliable Fail-safe System", Parallel and Distributed Systems, IEEE Computer Society, Vol. 47, No. 2, Feb. 1998, pp. 236-241.
- [8] N Laxmi Bhuyan, Yang Qing and Dharma PAgrawal., "Performance of Multiprocessor Interconnection

Networks", IEEE Computer, Vol. 22, Feb. 1993, pp. 25-37.

- [9] Nitin, "On analytic Bounds of Regular and Irregular Fault-tolerant Multistage Interconnection Networks", International Conference on PDPTA, June 2006, pp. 26-29.
- [10] V. Cherkassky, E. Opper, and M. Malek, "Reliability and fault diagnosis analysis of fault-tolerant multistage interconnection networks," in Proc. Fourteenth Int. Symp. Fault-Tolerant Comput, June 1984, pp. 246-251.
- [11] L. Ciminiera and A. Serra, "A fault-tolerant connecting network for multiprocessor systems," in Proc. Int. Conf. Parallel Processing, Aug. 1982, pp. 113-122.
- [12] R. Das and L. N. Bhuyan, "Reliability simulation of multiprocessor systems," in Proc. Int. Conf. Parallel Processing, Aug. 1985, pp. 591-598.
- [13] T-Y. Feng, "A survey of interconnection networks," IEEE Computer Mag., pp. 12-27, Dec. 1981.